
聯發科技- 黃睿夫 博士

講題: Low Power Design Technology for SOC

演講日期:104/11/23(一)

演講時間:下午 1:30-3:00

演講地點:台達館 103 室

簡歷

Rei-Fu Huang is a senior technical manager at MediaTek Inc. in Hsinchu City, Taiwan. He has been a memory designer around ten years, and focus on the memory testing and signoff solution of the advanced process in these years. His research interests include memory intellectual property design, testing, binning, and the signoff solutions. Huang has a BS and a PhD in electrical engineering from National Tsing-Hua University.

摘要

This talk is about the design technology in the low power SoC application. The content includes many technical information and correlated publications. It covers from the front-end to the back-end solutions.